



EXPRESS MAIL NO. EV347012835US

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Mollish

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Federico Pio
Application No. : 10/001,625 ✓
Filed : October 24, 2001 ✓
For : METHOD OF MANUFACTURING AN INTEGRATED
SEMICONDUCTOR DEVICE HAVING A PLURALITY OF
CONNECTION LEVELS

Examiner : Donghee Kang
Art Unit : 2811
Docket No. : 854063.512D1
Date : May 8, 2003

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

SUPPLEMENTAL AMENDMENT

Commissioner for Patents:

In response to the Office Action dated January 31, 2003, please amend the application as follows:

Amendments to the Claims are reflected in the listing of claims which begins on page 2 of this paper.

Remarks/Arguments begin on page 13 of this paper.

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